

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Junichi KONNO et al

Serial No.: 07/743,383 (Unofficial)

Group Art Unit:

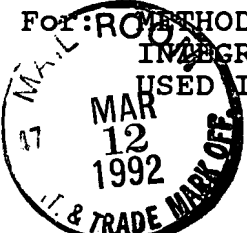
Filed: August 21, 1991

PCT Appln. No.: PCT/JP91/00861

Examiner:

Filed: June 26, 1991

For: METHOD FOR PRODUCING SEMICONDUCTOR
INTEGRATED CIRCUITS AND APPARATUS
USED IN SUCH METHOD

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner
Washington, D.C. 20231

March 12, 1992

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GROUP 150

Sir:

In compliance with applicants' duty of disclosure under 37 C.F.R. 1.56, enclosed herewith is a copy of the International Search Report and the references cited therein. The International Search Report was cited during the prosecution of the corresponding International patent application.

In the event that any fees are due with this paper, please charge our Deposit Account No. 01-2395.

Respectfully submitted,

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CMM:mms

Enclosures: PTO-FB-A820, International Search Report,
References (8)